

Observations – Part 1

7408 AND Gate Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	

7432 OR Gate Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	

7400 NAND Gate Truth Table:

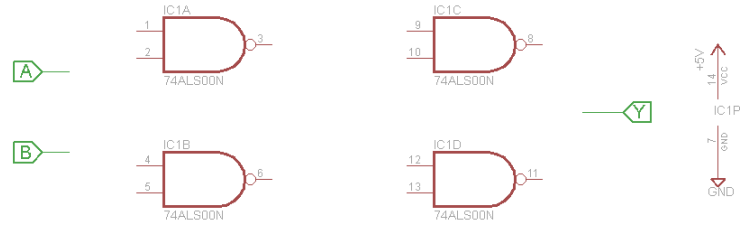
A	B	Y
0	0	
0	1	
1	0	
1	1	

7486 XOR Gate Truth Table:

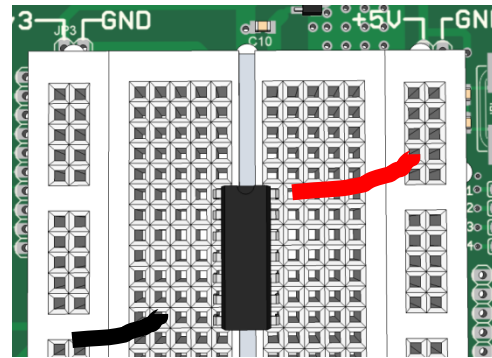
A	B	Y
0	0	
0	1	
1	0	
1	1	

OR Gate Built from NAND Gates:

Schematic:



Implementation:



Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	

Mystery Chip:

A	B	Y
0	0	
0	1	
1	0	
1	1	

My Mystery Chip number is _____. Based on the truth table it is a(n) _____ gate.

Observations – Part 2

AND2 Gate Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	

OR2 Gate Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	

NAND2 Gate Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	

XOR2 Gate Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	

XNOR2 Gate Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	

NOR2B1 Gate Truth Table:

A	B	Y
0	0	
0	1	
1	0	
1	1	